

ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

Title of Invention

Embedded Processor with Watchdog Timer for Programmable
Logic

Application Number :

Confirmation Number:

First Named Applicant: Andrew Crosland

Attorney Docket Number: 15114-053510

Art Unit:

Examiner:

Search string: (5687325 or 6260087 or 6467009 or RE34444 or 5970254).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass
	1	5687325	1997-11-11	Web Chang			
	2	6260087	2001-07-10	Web Chang			
	3	6467009	2002-10-15	Winegarden			
	4	RE34444	1993-11-16	Kaplinsky			
	5	5970254	1999-10-19	Cooke			

Signature

Examiner Name	Date